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Novocell Semiconductor Announces NVM IP Tape Out at IBM 45nm –
Brings 100 Percent Reliable Anti-Fuse One-Time Programmable Non-Volatile Memory to Advanced Node

April 4, 2012 - *Hermitage, PA,* Novocell Semiconductor, Inc., the leading innovator in high reliability one-time programmable (OTP) antifuse non-volatile memory (NVM), has announced the successful tape out of the firm's NovoBlox® family NVM at IBM 45nm SOI process. Novocell is pleased to have completed the advanced 45nm node with a confidential customer who commonly services markets ranging from military to heavy industrial to consumer electronics. The confidential customer chose IBM's foundry for this silicon project, and selected Novocell Semiconductor's intellectual property (IP) based upon the technology's reputation for 100% reliability, history of innovation, and licensing flexibility. Novocell continues their record of industry leadership with this announcement, now completing the offering of their NovoBlox family of antifuse OTP at the 45nm advanced node.

Walt Novosel, VP Engineering, noted, "We are pleased to publicly announce this latest demonstration of our long term tradition of innovation and leadership by demonstrating this first new product offering at this advanced node. Novocell continues to work closely with customers to offer our patented SmartBit™ NVM technology at the most cutting edge technologies."

Industry Leading Reliability and Manufacturing Ease

All Novocell NovoBlox family NVM IP products avoid the limitations of traditional embedded NVM technology by utilizing the patented dynamic programming and monitoring process and method of the Novocell SmartBit™, ensuring that 100% of customers' embedded bit cells are fully programmed. The result is Novocell's unmatched 100% yield and unparalleled reliability, guaranteeing customers that their data is fully programmed initially, and will remain so for an industry leading 30 years or more. Moreover, Novocell IP based on the SmartBit does not require complex manufacturing technology changes, but utilizes standard CMOS manufacturing processes, without masking or additional fabrication processes. Novocell NVM IP scales to meet all NVM size and complexity challenges that grow exponentially as SoCs continue to move to advanced nodes such as 45nm and beyond.

About NovoBlox® SmartBit™ Technology
Novocell's patented and uncontested SmartBit technology featuring a unique High Voltage Generator that eliminates the need for a large external charge pump, requires only the standard process I/O to achieve oxide breakdown, and is routinely fabricated within fully standard CMOS processes without need for additional layers, masks, or processing steps. The SmartBit employs a dynamic voltage programming method that senses when hard breakdown has been completed and triggers a DONE signal, providing customers unequalled assurance that the programmed data will be retained for 30 years.

About Novocell Semiconductor, Inc.
Novocell Semiconductor, Inc. specializes in developing and delivering advanced non-volatile memory intellectual property (IP) to the semiconductor industry. Novocell is the only provider of 2nTP, the first multi-time write antifuse memory IP. NovoBlox OTP, 2nTP, and NovoBits are the only antifuse memories proven to have zero tail bit failures within operating ranges and 30 years of data retention. The technology is available and scalable from 350nm to 45nm and beyond. For more information, please visit: www.novocellsemi.com.

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